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Griffin CID, Mesa Division CID, Mesa Division Ofc. Keith Griffin joined the Mesa Police Department in October of 2000. He has been assigned to the Mesa Division and has worked in the DUI and Investigative Sections. He has been trained in Spanish, German, and French. He has served on the SWAT Team, as well as, the Four Way Crawl Team, serving as Tactical Sergeant. His career started in Minneapolis Minnesota, where he graduated from the Minnesota Police Academy. He has also been a member of the San Diego Police Department. He has been a firearms instructor and is currently a certified FCW instructor. He is assigned to the Southwest Regional Office.

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Huawei...Huawei Ascend Y210 2G (Y210D) to 3G. How to install Huawei 2G Y210 firmware on the 3G Y210D or phone with. easy2root 2010 FW setup is given below. The present invention generally relates to comparators, and in particular to a technique for synchronizing a plurality of comparators such that they operate synchronously. Such comparators

are used in apparatus, for example, that sense the magnitude of the electric power of a television signal, convert it into digital data, and then feed the data to a microprocessor. In such apparatus, the output data of the comparator must be synchronized with the internal clock of the microprocessor, because the data must be processed in a definite order.

Therefore, in the past, the plurality of comparators are connected to a common clock source, and the timing of the data output from the comparators is synchronized with the clock. However, in the case of the apparatus, the microprocessor is physically separated from the comparators, so it is necessary to transmit clock signals to the comparators over a long distance.

Therefore, in the prior art, a plurality of comparators are connected to the same clock source, and a delay of the clock is compensated for by adding or subtracting

several clock cycles. However, in the case of the apparatus, the microprocessor is separated from the comparators, so the timing of data output from the comparators must be synchronized with the clock regardless of whether the apparatus is in operation or not. In the prior art, the timing of data output from the comparators can be synchronized with the clock by the use of an external timer circuit. However, the apparatus must include the timer circuit, and this complicates the apparatus